



ABV- Indian Institute of Information Technology & Management Gwalior  
(Autonomous Institute of Government of India)

Morena Link Road, Gwalior-474015, Madhya Pradesh, India

Website: <http://www.iiitm.ac.in>

ABV-IIITM/C2S/2025/Staff/Recruitment/02

May 27, 2025

**Advertisement for the Recruitment of Project Positions (JRF) under C2S**

Recruitment of the Junior Research Fellowship (JRF) through Interview under the MeitY, Gov. of India.  
Sponsored Projects: - **“Implantable Pacemaker Chip (iPACE-CHIP)”** and **“Designing energy-efficient AI accelerator for 3D object detection”** at ABV-IIITM Gwalior.

Interested candidates should submit dully completed prescribed application form along with the supporting document to [c2s@iiitm.ac.in](mailto:c2s@iiitm.ac.in).

Duration of the project: 5 Years

S. No.	Post	Qualifications	Salary
1.	Junior Research Fellowship-1  For iPACE-CHIP project.	M.Tech/M.E/M.S(ECE/CSE/EEE or equivalent) qualification recognized by the Institute with a minimum of 60% marks or CGPA of 6.5 on a 10-point scale in the qualifying degree. Additionally, the applicants must have qualified GATE/NET exam.  Who have only B.Tech. degree are also eligible if they have 75% marks or 8.0 CGPA in B.Tech/B.E. and have qualified GATE/NET exam. The requirement for GATE can be exempted for the applicants having B.Tech. or Equivalent degree from Centrally Funded Technical Institutions (CFTIs) with CGPA 8.0 or above at B.Tech. level.	INR 37,000/- + HRA  HRA will be given as per GoI. norms.
2.	Junior Research Fellowship-2  For AI-Accelerator project.	M.Tech/M.E/M.S(ECE/CSE/IT or equivalent) qualification recognized by the Institute with a minimum of 60% marks or CGPA of 6.5 on a 10-point scale in the qualifying degree. Additionally, the applicants must have qualified GATE/NET exam.  Who have only B.Tech. degree are also eligible if they have 75% marks or 8.0 CGPA in B.Tech/B.E. and have qualified GATE/NET exam. The requirement for GATE can be exempted for the applicants having B.Tech. or Equivalent degree from Centrally Funded Technical Institutions (CFTIs) with CGPA 8.0 or above at B.Tech. level.	INR 37,000/- + HRA  HRA will be given as per GoI. norms.

## **Applicants are expected to pursue Ph.D. in EEE Dept. satisfying the institute eligibility criteria**

### **Terms and conditions:**

1. The above positions are purely contractual. The initial appointment will be for 1 Year and it may be extended further based on the satisfactory performance of the candidate till the duration of the project.
2. The candidate must submit the application form along with the supporting documents to [c2s@iiitm.ac](mailto:c2s@iiitm.ac). On or before 03.06.2025.
3. After shortlisting, the reporting date and time for the interview will be communicated via email by the Department of Electrical and Electronics Engineering, ABV-IIITM Gwalior.
4. Candidates have to appear for interview with CV, two passport-size photo, original certificates and attested photocopies of all certificates, mark sheets, degrees and testimonials.
5. The Outstation candidates can appear online for interviews. They will send a request to appear online for interview, along with self-attested copies of all relevant original documents followed by their CV and send the above information to [c2s@iiitm.ac.in](mailto:c2s@iiitm.ac.in).
6. The candidates fulfilling the minimum eligibility criteria will only be interviewed.

Dr. Manisha Pattanaik (Chief Investigator)

Email: [c2s@iiitm.ac.in](mailto:c2s@iiitm.ac.in)

Phone: 0751-2449812 and 9425619281

### **ELIGIBILITY**

1. M.E./M.Tech. in ECE/CSE/VLSI/Embedded System/Microelectronics or its equivalent with at least 6.5 CGPA or 60% marks in aggregate from a recognized technical institute or university, and B.E./ B.Tech in ECE/CSE/EEE or equivalent with at least 6.5 CGPA or 60% marks in aggregate from a recognized technical institute or university and qualified GATE/NET exam. Those who have exposure to EDA tools will be preferred.
2. Those who have only B.E /B. Tech in (ECE/EEE/CSE/IT or equivalent) with 75% marks or CGPA of 8.0 and qualified GATE/NET exam are also eligible. The requirement for GATE can be exempted for the applicants having B.Tech. or Equivalent degree from Centrally Funded Technical Institutions (CFTIs) with CGPA 8.0 or above at B.Tech. level. Those who have exposure to EDA tools will be preferred.

### **APPLICATION CHECKLIST (AT THE TIME OF INTERVIEW)**

Hard copy of the application form (duly signed) must be produced with following self-attested documents at the time of interview:

1. Photocopy of marks sheet/Grade card/certificate of the secondary (10), higher secondary (10+2), B.E/B.Tech, and/or M.E/M. Tech.
2. Photocopy of the proof of the date of birth.

Atal Bihari Vajpayee-Indian Institute of Information Technology and Management  
(ABV-IIITM) Gwalior



APPLICATION FORM

Junior Research Fellow (JRF) under C2S PROJECT

Affix  
Passport  
Size Photo  
Here

1.	Name					
2.	Father's Name					
3.	Date of Birth					
4.	Address for correspondence					
5.	Category: Gen./ SC/ PwD / OBC					
6.	E-mail ID					
7.	Mobile No.					
8.	Project Position Applied:					
9.	Educational Qualification	Branch/ Field	% of Marks or CGPA	Division	University	Year of passing
	Bachelor's Degree					
	Master's Degree					
	Any other qualification					
	Hardware/ Software skill					
10.	GATE/NET	Specialization	Year Appeared	Score	All India Rank	
11.	Experience	Teaching	Research	Industry		
		____Y____M	____Y____M	____Y____M		
12.	Research Papers (Nos.)	Journals		Conference Proceeding		
		SCI/SCIE	Scopus	National	International	
13.	M. Tech. Thesis Title					
	B. Tech. Project Title					
14.	Any other relevant details					

Date:

Signature of Applicant

(For office use only)

REMARKS OF THE VERIFICATION COMMITTEE:

Signature of Verification Committee