



विश्वजीवनामृतं ज्ञानम्



ACCELERATE
विद्युज्जान

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY & MANAGEMENT, GWALIOR

(AN INSTITUTE OF NATIONAL IMPORTANCE, MINISTRY OF EDUCATION, GOVERNMENT OF INDIA)

INVITING APPLICATION FOR



SERB-SPONSORED ACCELERATE VIGYAN- TRAINING AND SKILL INTERNSHIP (VRITIKA)

ON

MACHINE LEARNING TOOLS & TECHNIQUES FOR AUDIO SIGNAL PROCESSING

20th June - 20th July 2023

**Total Participants: 05 (only PG, PhD and
4th Year UG Students)**

No Registration Fee



Workshop Objective

This internship aims to provide a training/learning/skill level platform for PG and Ph.D. students from diverse fields to train them with significant breakthroughs in machine learning and audio signal processing. Attendees can expect to explore topics such as automatic speech recognition, music information retrieval, generative models for speech, and music synthesis.

Registration Details and Selection Process

- Register using this link or scan the QR-code
<https://forms.gle/2QouFE4E2gS1yzbh8>
- Please fill out the Google form along with the NOC letter.
- The selection of interns is based on the recommendations of the Selection Committee of the EEE Department.
- Only selected candidates will be informed by email; therefore, the candidates must provide valid E-mail-IDs during online registration.
- The selected candidates will have to acknowledge and accept the offer by email within 48hrs, failing to which the waiting listed candidates shall be accommodated.

Interns-Eligibility

Students of MTech./M.Sc./Ph.D./UG 4th-year of Electronics Communication Engineering, Computer Science, or other relevant specialization are eligible to apply.

Terms and Conditions

- The selected interns are required to attend the program physically.
- The interns are expected to work on the assignments assigned by the event organizer.
- Training report to be prepared and submitted on the day of completion of the internship.
- The institute will reserve the IPR of the research work, and can not be used by the intern without permission of the concerned authority.
- The selected candidates will be provided food and accommodation in the institute hostel/guest house.
- The selected participants are eligible for TA (shortest route 3rd AC class train fare) reimbursement on submission of tickets, according to the norms.

About The Department

The Department of Electrical and Electronics Engineering is one of the major departments being created in 2022 to achieve the mission and vision of the institute. The Department is engaged in teaching and research activities in the areas of VLSI, SoC design, wireless communication, multimedia signal processing, and communication systems. Currently, the department offers M.Tech. in VLSI and Embedded Systems and Ph.D. program in relevant areas.

**Last Date for Registration:
15th June 2023**

EVENT ORGANIZER

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