

Workshop
On
Advance Trends in VLSI
Industry
(November 12-16, 2021)



विश्वजीवनामृतं ज्ञानम्

Patron

Prof. (Dr.) Rajendra Sahu
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Coordinators

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Supported by

Science & Engineering Research Board (SERB), A statutory body of the Department of Science & Technology, Govt. of India under the grant of Scientific Social Responsibility.

Advisory committee

1. Prof. G. K. Sharma, Dept of IT
2. Prof. M. Pattanaik, Dept of IT
3. Dr. Vinal Patel, Dept of IT
4. Dr. Santosh Singh Rathore, Dept of IT
5. Dr. Sunil Kumar, Dept. of IT

About ABV-IIITM Gwalior

ABV-Indian Institute of Information Technology and Management (IIITM) in Gwalior, Madhya Pradesh is an autonomous institute set up by Ministry of Human Resource Development (MHRD), Govt. of India in 1997. It is an effort by MHRD towards creating professionals in areas of management and information technology from the same institute. This institute was created for facilitating higher education, research, and consultancy in areas of information technology (IT) and business management. The institute is located on a 150-acre campus. The Institute has established research collaborations with various Universities in USA, France, and Japan.

The department of Information & Technology Offers M.Tech. in VLSI & Embedded System Design.

Science & Engineering Research Board

Science & Engineering Research Board (SERB), A statutory body of the Department of Science & Technology, Govt. of India offer the grant under Scientific Social Responsibility. The objective of the above program is to translating the scientific knowledge to improve the employability.

Who Can Participate

Final year students of U.G., M Tech. and Ph. D. scholars of UGC/AICTE recognized Institutes.

About the Workshop

The objective of the Online Workshop is to give the idea from Post Layout to Chip Tapeout using Cadence, address the research, development and design problems and advance their solutions

in VLSI domain and give the idea to initialize the start-up. The various Topics to be covered in the workshop are:

VLSI Design

- In-memory computing
- Advanced ADC Design
- VLSI Physical Design
- Analog Design
- FPGA Design
- Deep Learning
- Machine learning application in VLSI
- Initialize the start-up
- Grants for the start-up

Resource Persons

- Prof. Jawar Singh (IIT Patna)
- Prof. Hitesh Shrimali (IIT Mandi)
- Mr. Narendra Sen / Founder & CEO Rackbank,
- Expertise from Renowned VLSI Industry
- Prof. M. Pattanaik (ABV-IIITM Gwalior)
- Dr. Bharat Gupta (NIT Patna)
- Dr. Vinal Patel (ABV-IIITM Gwalior)
- Dr. Santosh Singh Rathore (ABV-IIITM Gwalior)
- Dr. Suneel Kumar (ABV-IIITM Gwalior)
- Dr. Gaurav Kaushal (ABV-IIITM Gwalior)

Registration and Link for Joining the Meeting

There is **No Registration Fees**. Registration link, Brochure is available in website: iiitm.ac.in/index.php/en/. The same registration link is https://docs.google.com/forms/d/e/1FAIpQLSdpqRFsm5PkUPJbWGS7Y_vCl_dIthR-z0gUW-7zGiX5wT8GpUA/viewform?usp=sf_li nk The link for joining the meeting is <https://iiitm.webex.com/iiitm/j.php?MTID=m420a9ed68b599ffd9c283a21b06de27d>